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Sheet	1	of	2
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Application Number	10/657,605
Filing Date	September 08, 2003
First Named Inventor	Huy M. Nguyen
Art Unit	2819
Examiner Name	Examiner Cho
Attorney Docket Number	RA311 (RA311.P.US)

U. S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

**Examiner
Signature**

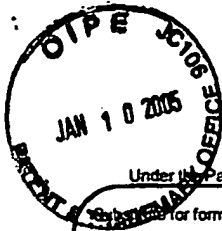
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Complete if Known	
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
<i>jk</i>	4	GABARA, THADDEUS J. et al. "A 200 MHz 100K ECL Output Buffer for CMOS ASICs." 1990 IEEE. Pages 4. Sept. 1990	
	5	GABARA, THADDEUS J., "On-Chip Terminating Resistors for High Speed ECL-CMOS Interfaces." February 1992. IEEE. Pages 292-295.	
	6	KNIGHT, THOMAS F. JR., "A Self-Terminating Low-Voltage Swing CMOS Output Driver." IEEE Journal of Solid-State Circuits, Vol. 23, No. 2, April 1988. pgs. 457-464.	
	7	GABARA, THADDEUS J., "Digitally Adjustable Resistors in CMOS for High-Performance Applications." IEEE Journal of Solid-State Circuits, Vol. 27, No. 8, Aug. 1992. Pgs. 1176-1185.	
	8	BABCOCK, J.A., "Precision Electrical Trimming of Very Low TCR Poly-SiGe Resisitors." IEEE Electron Device Letters, Vol. 21, No. 6, June 2000. pgs. 283-285.	
<i>✓</i>	9	SHAH, SUNAY et al., "A Temperature Independent Trimmable Current Source." Department of Engineering Science, University of Oxford. ISCAS 2002. 4 pages. May 2002	
<i>jk</i>	10	KIM, SU-CHUL, "Programmable Digital On-Chip Terminator." ITC-CSCC, 2002. 4 pages. No MONTH	

Examiner Signature	<i>Examiner Cho</i>	Date Considered	5/14/05
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Examiner Name	James Cho
Attorney Docket Number	RA311 (RA311.P.US)

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